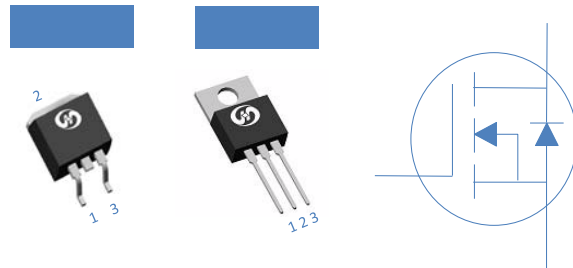




DS(on), typ			Ω
DS(on), typ			Ω



=25 (unless otherwise specified)

		=25		
		=100		
		=25		
		=25		
Power Dissipation		=25		W
Operating and Storage Temperature				

	JC		W
	JA		W



=25 (unless otherwise specified)

				typ		
		μ				
		μ				
		=25				μ
		=100				
						Ω
	fs					
		Open, f=1MHz				Ω

Input Capacitance						
Output Capacitance		=75V, f=1MHz				pF
Reverse Transfer Capacitance						
Turn off Delay Time	d(off)	Ω				
	f					

		$\Delta t=100A/\mu$				



Hiteck



Fig 1. Typical Output Characteristics	
D	Figure 4. Normalized On-Resistance vs. Junction Temperature
Figure 5. Typical Transfer Characteristics	Figure 6. Typical Source-Drain Diode Forward Voltage



Figure 7. Typical Gate-Charge vs. Gate-to-Source Voltage

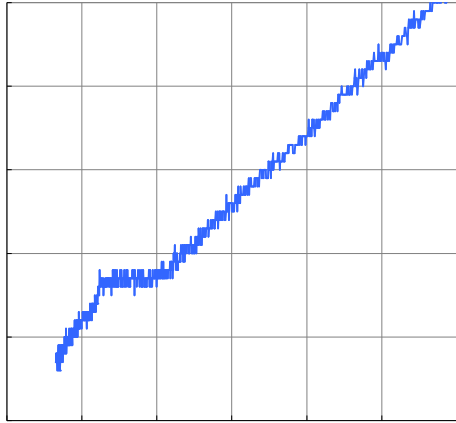


Figure 8. Typical Capacitance vs. Drain-to-Source Voltage

Figure 9. Maximum Safe Operating Area

Figure 10. Maximum Drain Current vs. Case Temperature

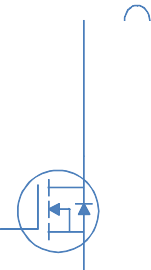
Figure 11. Normalized Maximum Transient Thermal Impedance, Junction-to-Case



--	--

--	--

Uclamped Inductive Switching (UIS) Test

	
---	--

--	--

